



FIG. 2

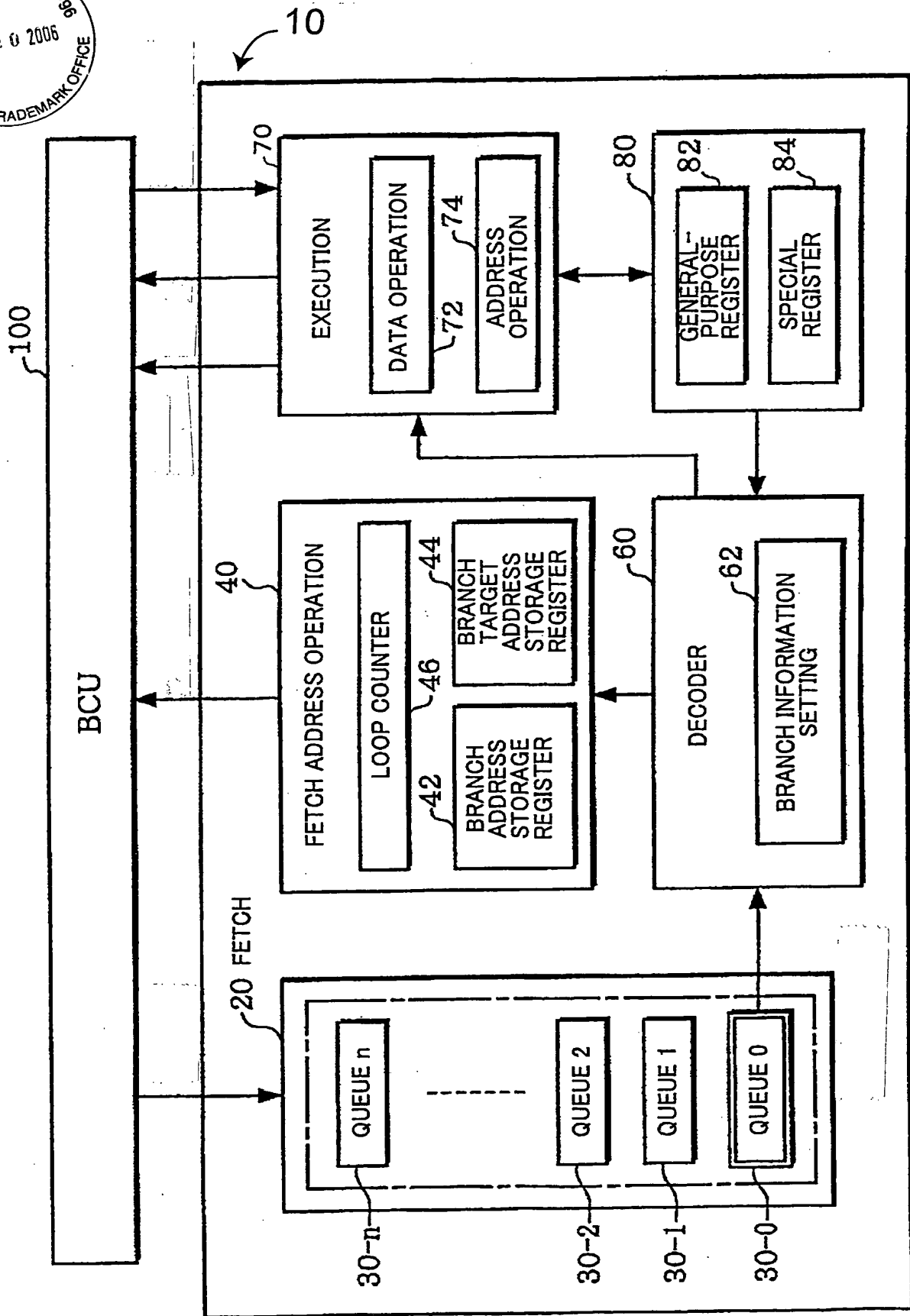


FIG. 3A

jprr x, y

FIG. 3B

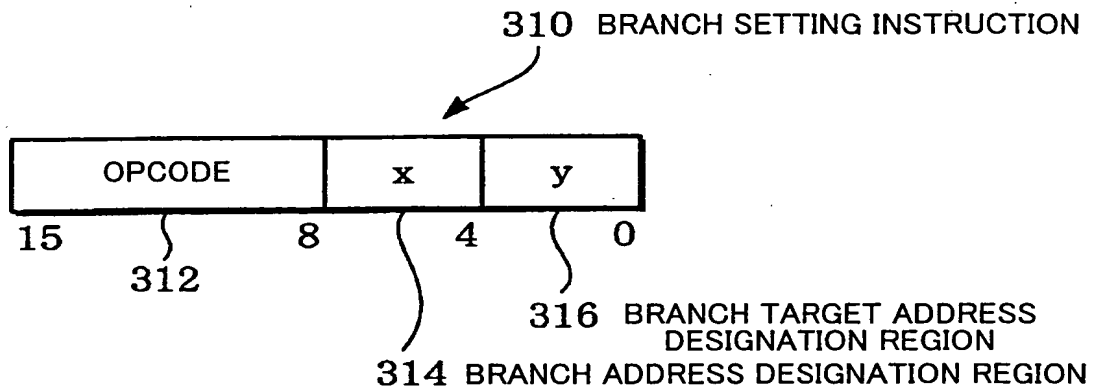
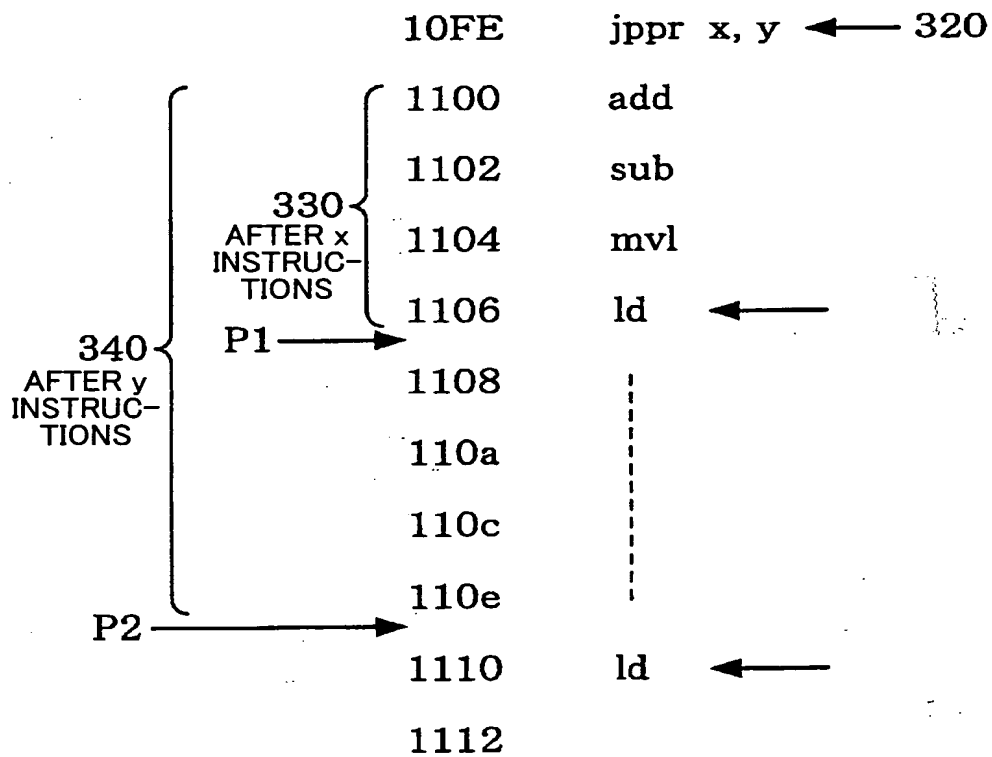


FIG. 3C



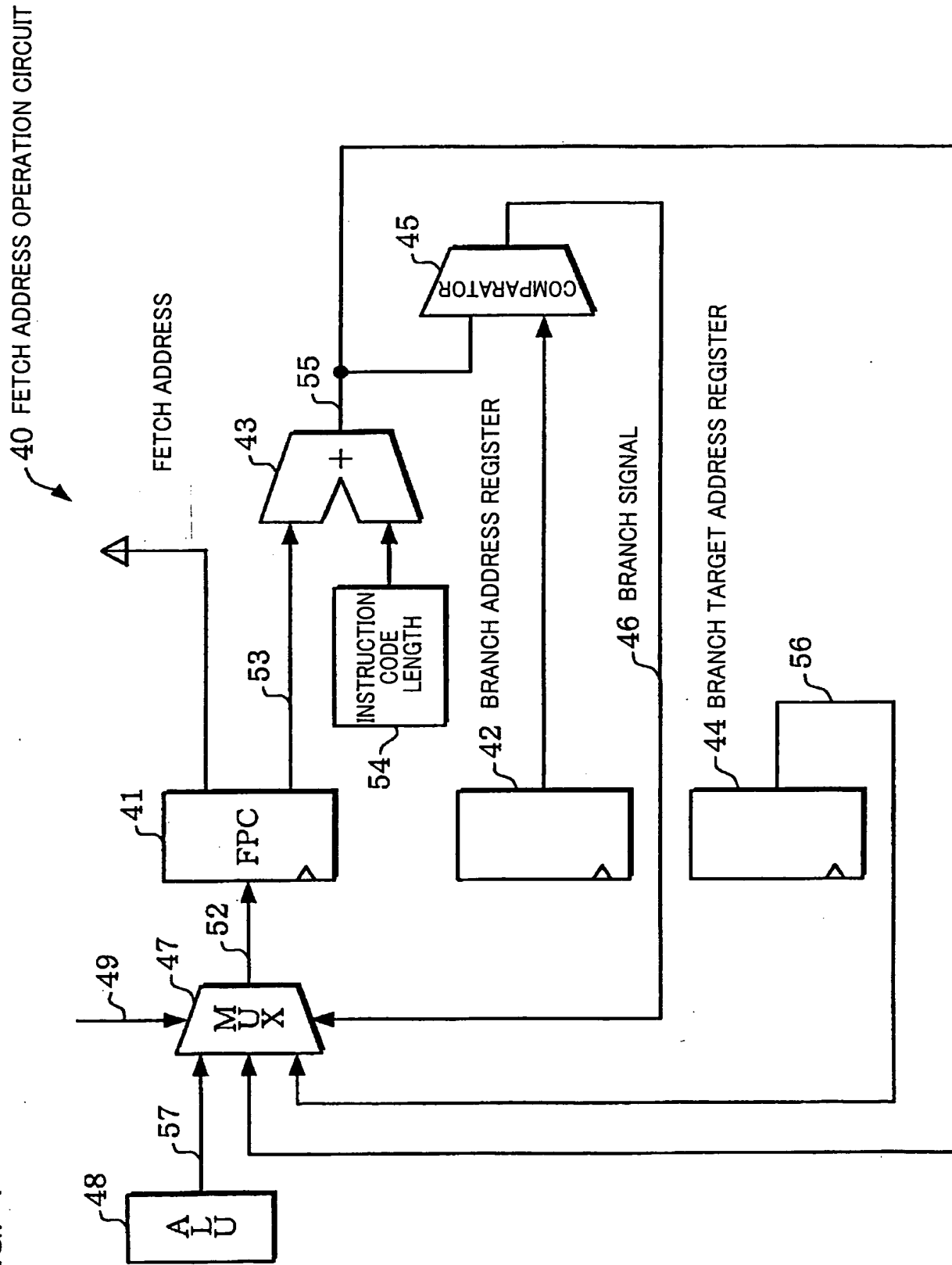


FIG. 5A

loop x, y, z

FIG. 5B

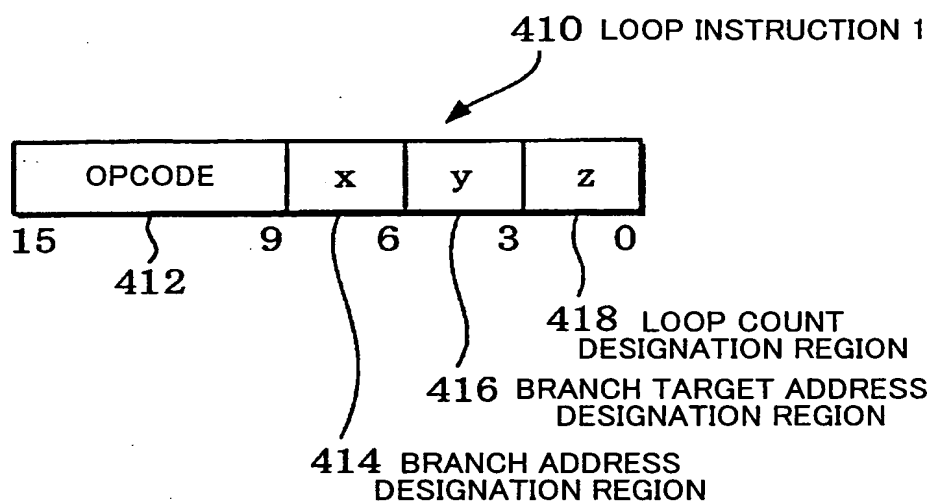


FIG. 5C

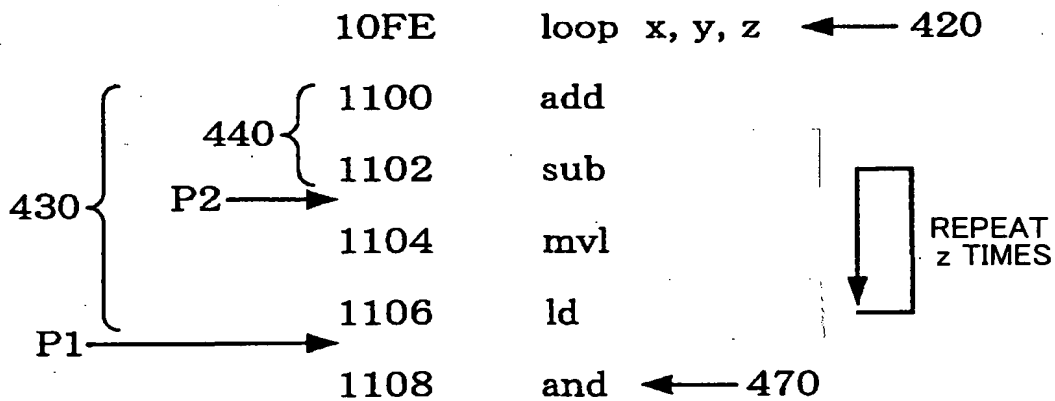


FIG. 6

40' FETCH ADDRESS OPERATION CIRCUIT

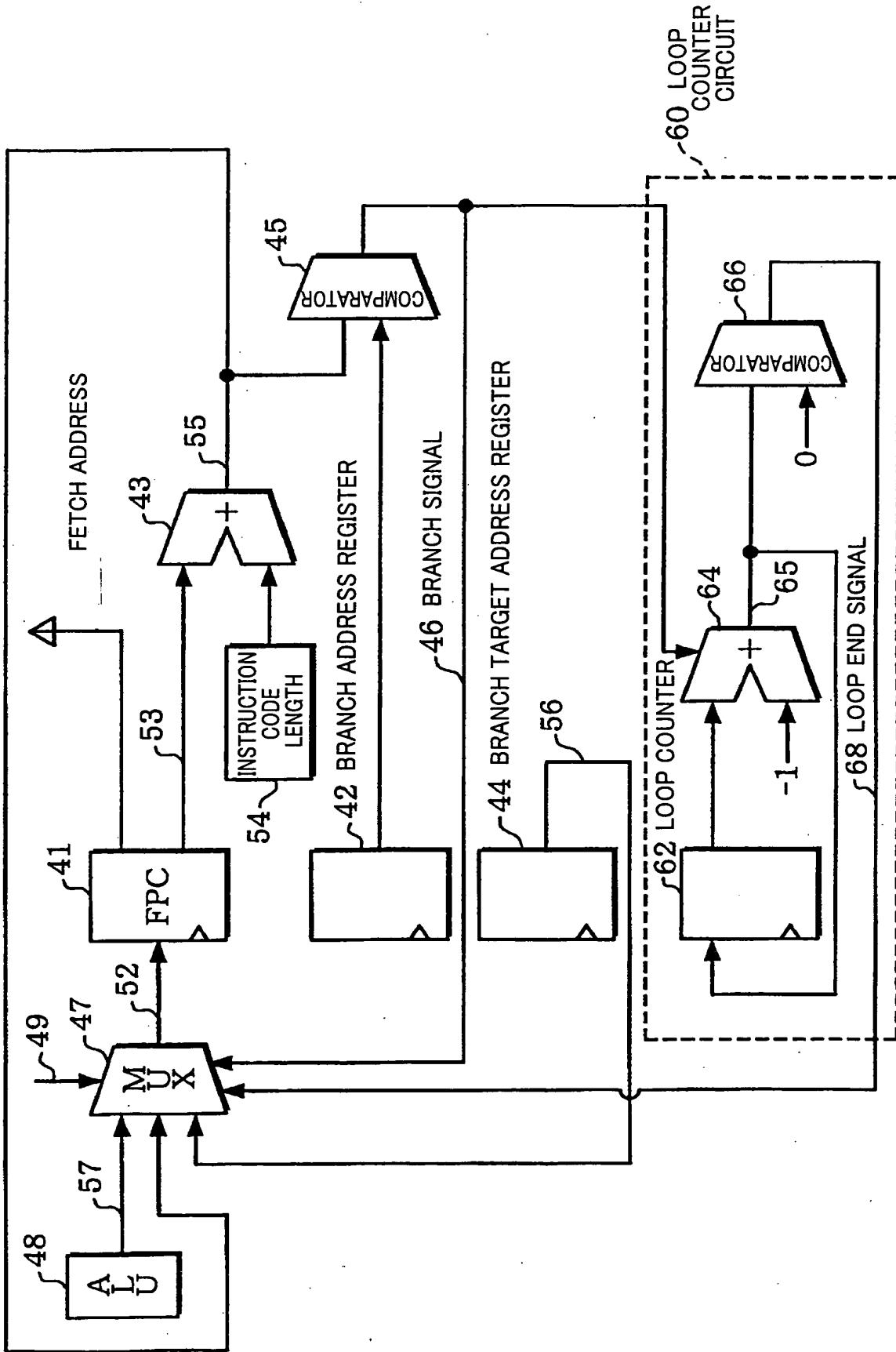


FIG. 7A

loop x, z

FIG. 7B

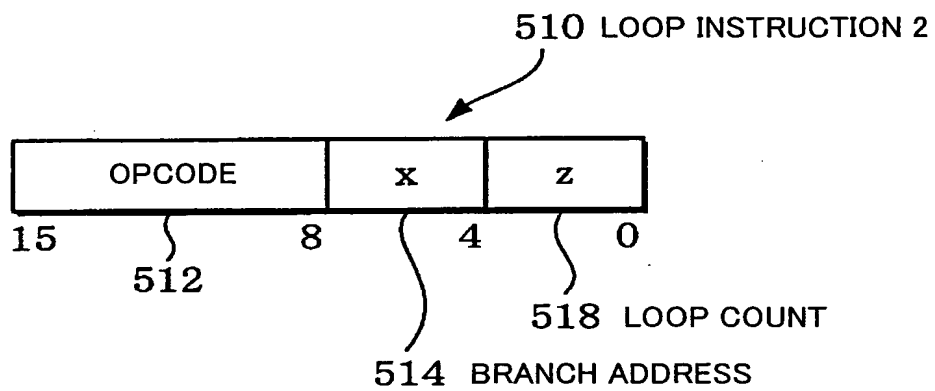


FIG. 7C

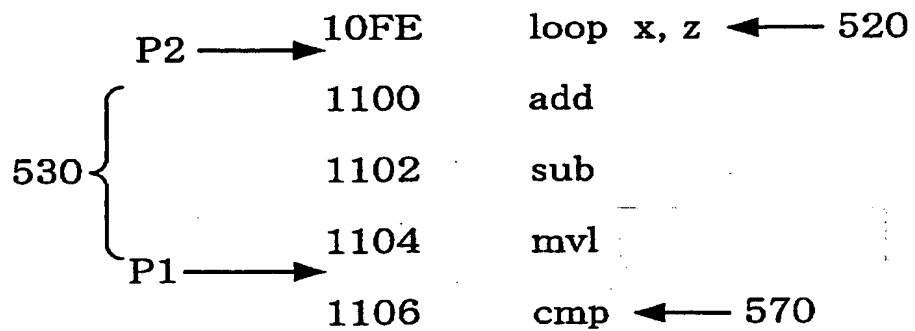


FIG. 9A

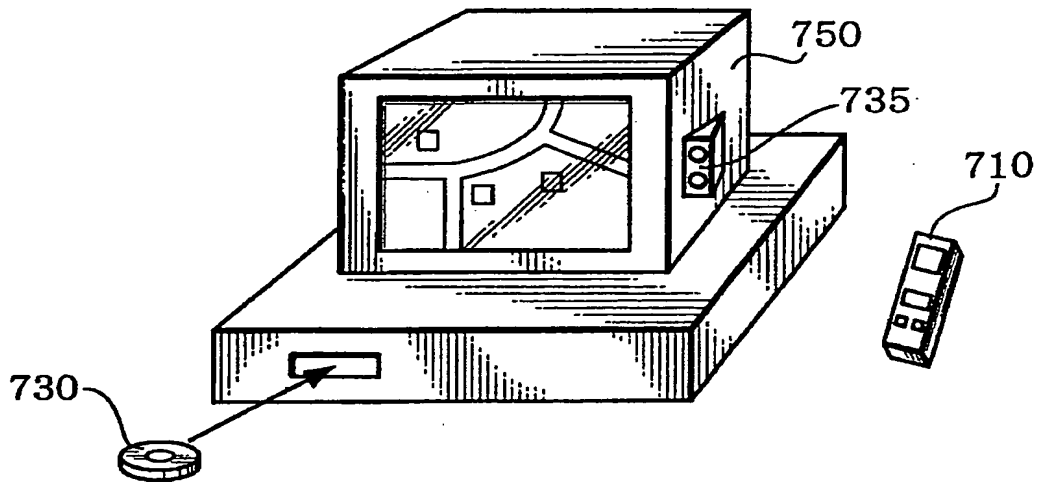


FIG. 9B

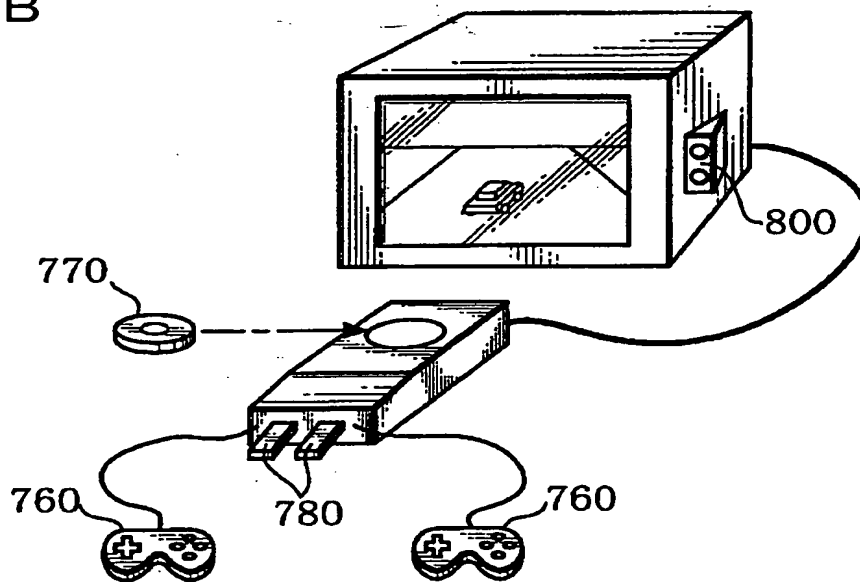


FIG. 9C

